

全球領航專家齊聚 探討最新半導體趨勢，掌握市場商機

由工業技術研究院主辦，引領半導體產業發展的年度盛會「2023國際超大型積體電路技術研討會」，齊聚國際半導體大廠與國內外頂尖學術機構產學研代表，逾60位知名專家分享精彩演講及論文發表，聚焦半導體產業最新產業趨勢及技術發展，讓您為下世代產業發展及多元應用提前布局，搶攻市場商機。



Olivier Faynot

Head of Silicon Component
Division, CEA-Leti



Shih-Lien Lu

Professor
Warner Pacific University



John Martinis

Professor
UC Santa Barbara



Robert Munoz

Principal Engineer
Intel



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台灣區總經理
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Ken Takeuchi

Professor
The University of Tokyo



王鈺強

AI 研究總監
NVIDIA

研討會焦點內容

- 精選全球頂尖論文68篇
- 2場聯合主題議程
 - Sensors – Devices Circuit and Applications in Automotive/Drone
 - Heterogeneous Integration
- 7場特別主題議程
 - High Power Devices
 - Advanced Packaging Technologies
 - Novel Channel Logic and 3D-stacked Transistors
 - Dielectric Stacking and Interface Engineering
 - Compute-in-Memory: From Architecture to Devices
 - Security and Encryption
 - Advanced Process-induced Design Challenges and Solutions
- 3場深度短期課程
 - BEOL Oxide Semiconductor Devices
 - AI Fundamentals and Practices
 - Data Converter Design for Emerging Applications
- 2場國內IC設計廠商領先技術之研究成果(Industrial Sessions)
 - Enabling Practices of Silicon Life-cycle Management
 - Cutting-Edge Computing Technologies from Purpose-Built Accelerators, Unified Memory Interfaces, to AI SoC Design Flows

大師專題演講

- **Plenary Session 1**
 - **John Martinis**, Professor, UC Santa Barbara
 - **Robert Munoz**, Principal Engineer, Intel
- **Plenary Session 2**
 - **Olivier Faynot**, Head of Silicon Component Division, CEA-Leti
 - **王鈺強**, AI研究總監, NVIDIA
- **Plenary Session 3**
 - **Ken Takeuchi**, Professor, The University of Tokyo
 - **Shih-Lien Lu**, Professor, Warner Pacific University
- **Luncheon Keynote**
 - **宋栢安**, 台灣區總經理, Cadence



VLSI TSA研討會將依循中央流行疫情指揮中心防疫規定，若您於研討會前兩天內有發燒或呼吸道症狀者，請改為參加線上研討會；本研討會期間，請於室內全程配戴口罩，後續防疫規定，將視疫情變化做滾動調整及通知。

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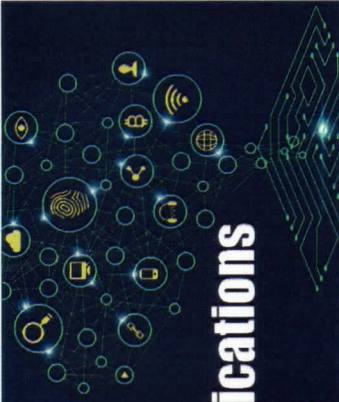
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2023 VLSI International

Symposium on Technology, Systems and Applications

April 17-20, 2023 Hsinchu, Taiwan



Joint Session
 TSA Session
 DAT Session

Time	Monday, April 17	Tuesday, April 18	Wednesday, April 19	Thursday, April 20
09:00			J3: Plenary Session 08:30 J3-1 Disruptive Approaches Towards Energy Efficient VLSI Technologies Dimitar Paynot CEA-Leti	J6: Plenary Session 08:30 J6-1 Neuromorphic Computing with Computation-in-Memory (CIM) Ken Takemichi The University of Tokyo
09:50	Opening & Award Ceremony		10:10 J3-2 Multimodal AI: How It's Changing Our Lives? Frank Wang NVIDIA	10:10 J6-2 VLSI Design and Test View of Computer Security Shih-Lien Lu Warner Pacific University
10:10	J1: Plenary Session 10:10 J1-1 Building a Quantum Computer John Martinis UC Santa Barbara			
11:50	11:00 J1-2 Industry Scale Reuse in the Chiplet Era Robert Muroz Intel			
12:50				
13:40	T1: Poster Session (Ballroom D / 11th Floor) Time for exchanging ideas with the poster presenters		J4: Luncheon Keynote (Ballroom B / 10th Floor) The Revolution of AI in Intelligent System Design Brian Sung, Candence	
12:30				
17:15				